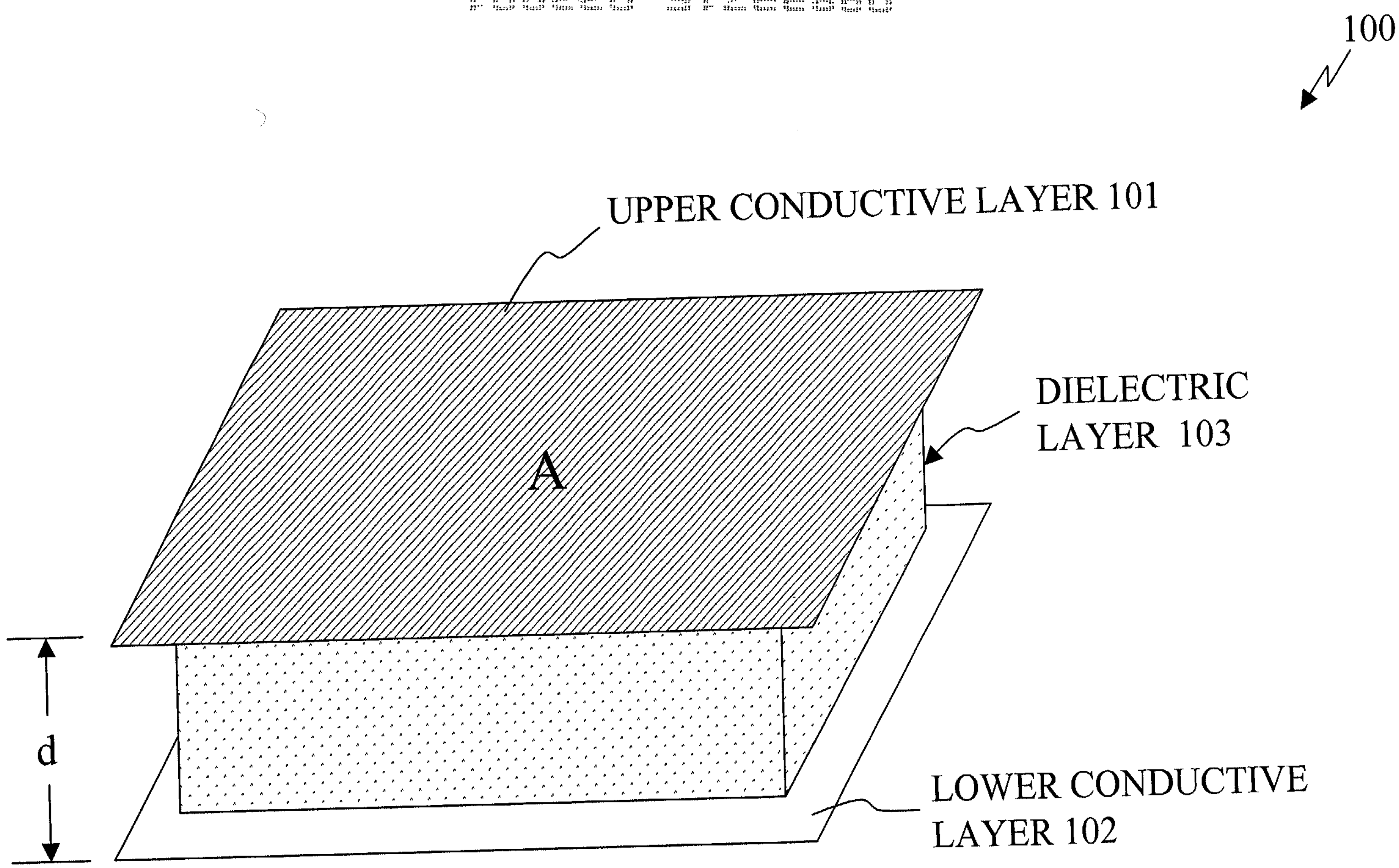

PARALLEL PLATE CAPACITOR

(PRIOR ART)

Fig. 1

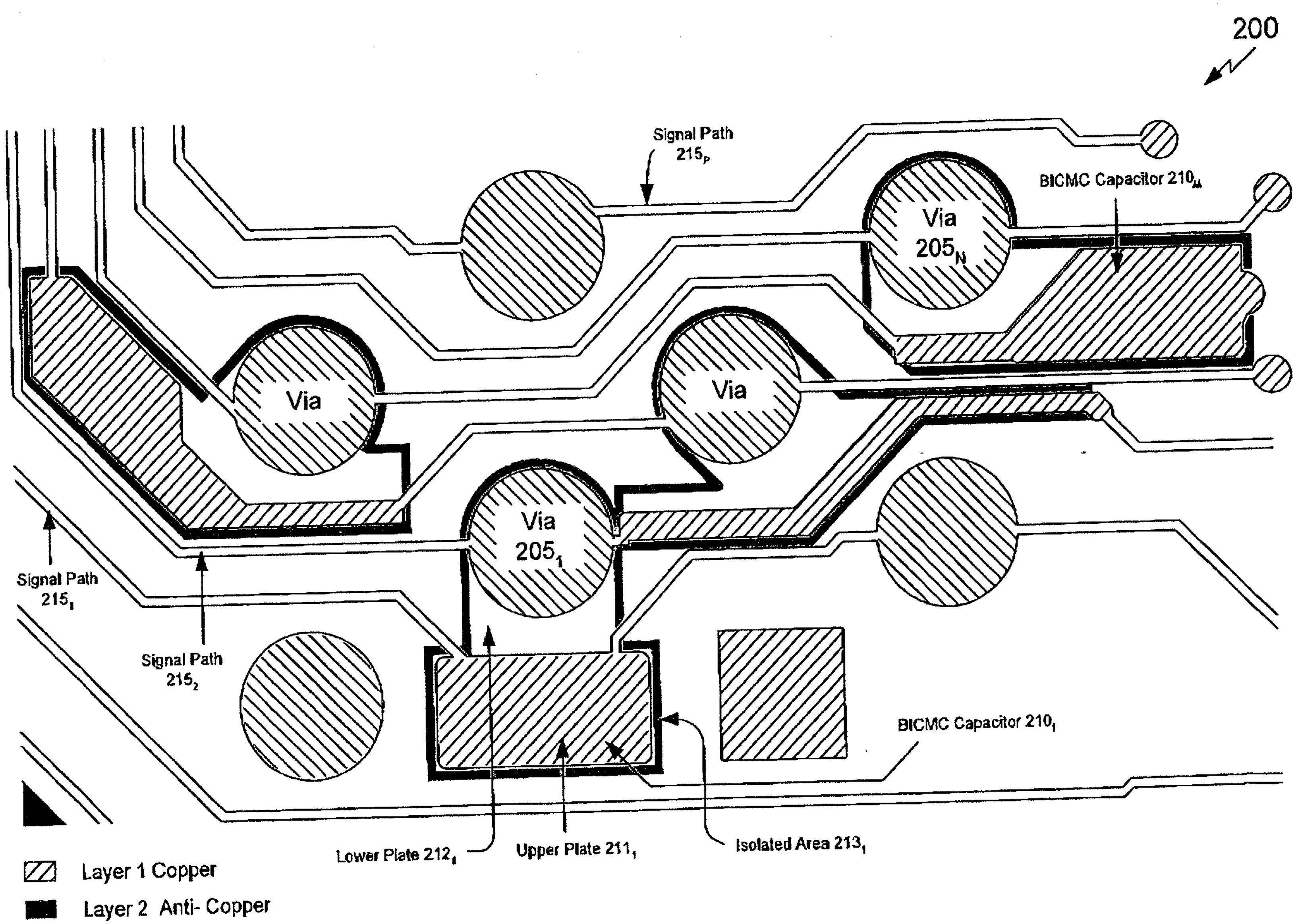
200
Signal Path
215_P
BICMC Capacitor 210_M
Via
205_N
Via
Via
Via
205_1
Signal Path
215_1
Signal Path
215_2
BICMC Capacitor 210_1
Lower Plate 212_1
Upper Plate 211_1
Isolated Area 213_1
Layer 1 Copper
Layer 2 Anti- Copper

Fig. 2

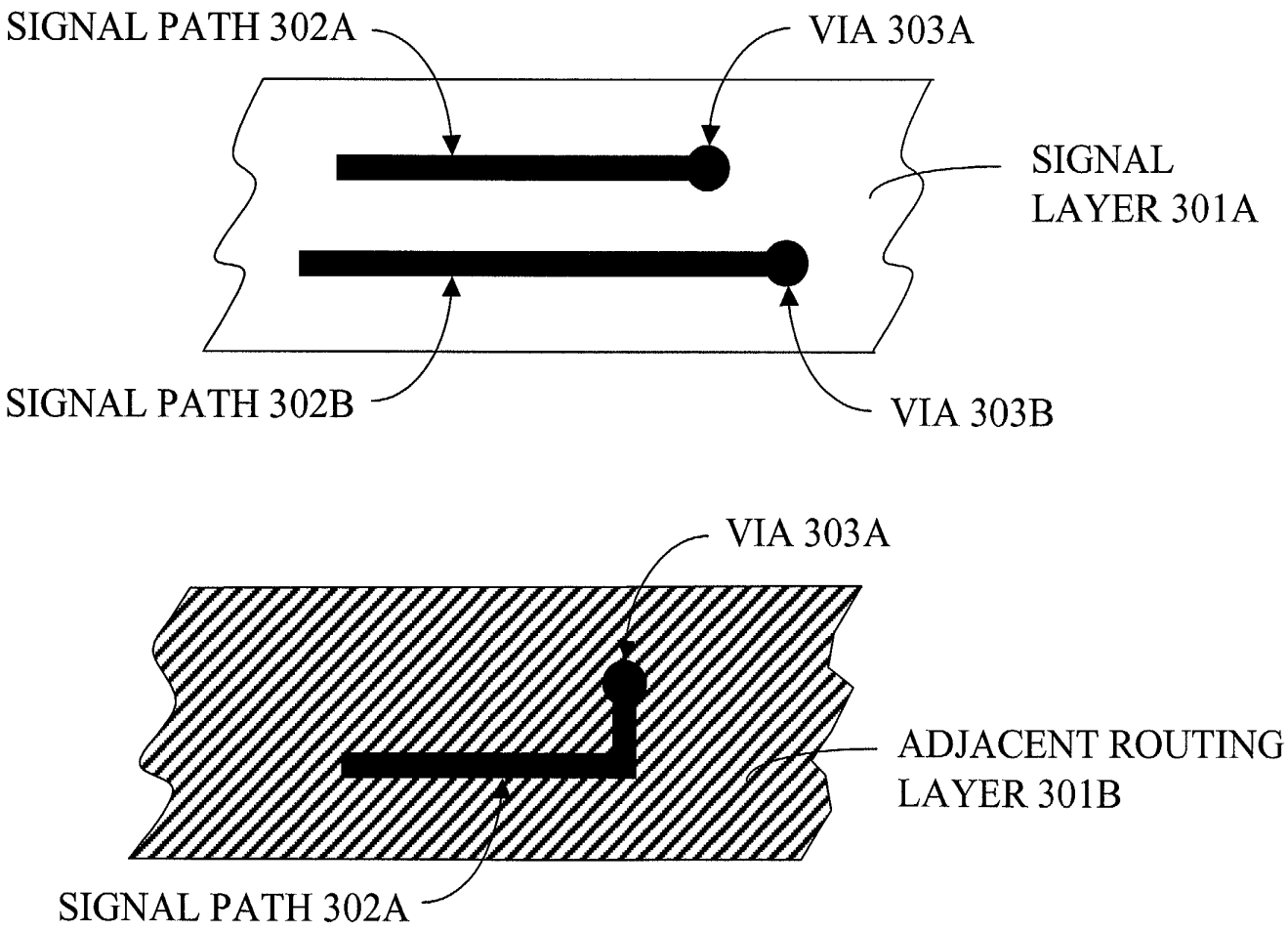
SIGNAL PATH 302A
VIA 303A
SIGNAL
LAYER 301A
SIGNAL PATH 302B
VIA 303B
VIA 303A
ADJACENT ROUTING
LAYER 301B
SIGNAL PATH 302A

Fig. 3